

L Number	Hits	Search Text	DB	Time stamp
1	1460	710/305-307.ccls. or 710/309-310.ccls. or 710/313.ccls. or 710/315.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/05 16:08
2	1764	710/52.ccls. or 710/62-63.ccls. or 710/66.ccls. or 710/74.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/05 16:09
3	24052	interfac\$3 near6 (chip or asic or integrated)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/05 16:10
5	1141	(interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/05 16:30
6	87028	(microprocessor or cpu or processor) near10 interfac\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/05 16:12
7	70	((microprocessor or cpu or processor) near10 interfac\$4) and (((memory or SDRAM or RDRAM or (synchronous adj2 DRAM) or (rambus)) near5 interfac\$4) and (((interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)) and (((interfac\$3 near6 (chip or asic or integrated)) near10 (microprocessor or processor or cpu)) SAME ((interfac\$3 near6 (chip or asic or integrated)) near10 memory)))) and (@ad<19990929 or @rlad<19990929)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/05 16:18
8	1260	711/105.ccls. or 711/100.ccls. or 711/212.ccls.	USPAT; US-PGPUB	2002/09/05 16:20
9	1	((microprocessor or cpu or processor) near10 interfac\$4) and (((memory or SDRAM or RDRAM or (synchronous adj2 DRAM) or (rambus)) near5 interfac\$4) and (((interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)) and (((interfac\$3 near6 (chip or asic or integrated)) near10 (microprocessor or processor or cpu)) SAME ((interfac\$3 near6 (chip or asic or integrated)) near10 memory)))) and (@ad<19990929 or @rlad<19990929)) and (711/105.ccls. or 711/100.ccls. or 711/212.ccls.)	USPAT; US-PGPUB	2002/09/05 16:21
10	2988	(710/305-307.ccls. or 710/309-310.ccls. or 710/313.ccls. or 710/315.ccls.) or (710/52.ccls. or 710/62-63.ccls. or 710/66.ccls. or 710/74.ccls.)	USPAT; US-PGPUB	2002/09/05 16:21

11	4	(((((microprocessor or cpu or processor) near10 interfac\$4) and (((memory or SDRAM or RDRAM or (synchronous adj2 DRAM) or (rambus)) near5 interfac\$4) and (((interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)) and (((interfac\$3 near6 (chip or asic or integrated)) near10 (microprocessor or processor or cpu)) SAME (((interfac\$3 near6 (chip or asic or integrated)) near10 memory))))) and (@ad<19990929 or @rlad<19990929)) and ((710/305-307.ccls. or 710/309-310.ccls. or 710/313.ccls. or 710/315.ccls.) or (710/52.ccls. or 710/62-63.ccls. or 710/66.ccls. or 710/74.ccls.))	USPAT; US-PGPUB	2002/09/05 16:21
12	4	(((((microprocessor or cpu or processor) near10 interfac\$4) and (((memory or SDRAM or RDRAM or (synchronous adj2 DRAM) or (rambus)) near5 interfac\$4) and (((interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)) and (((interfac\$3 near6 (chip or asic or integrated)) near10 (microprocessor or processor or cpu)) SAME (((interfac\$3 near6 (chip or asic or integrated)) near10 memory))))) and (@ad<19990929 or @rlad<19990929)) and ((710/305-307.ccls. or 710/309-310.ccls. or 710/313.ccls. or 710/315.ccls.) or (710/52.ccls. or 710/62-63.ccls. or 710/66.ccls. or 710/74.ccls.))) not (((((microprocessor or cpu or processor) near10 interfac\$4) and (((memory or SDRAM or RDRAM or (synchronous adj2 DRAM) or (rambus)) near5 interfac\$4) and (((interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)) and (((interfac\$3 near6 (chip or asic or integrated)) near10 (microprocessor or processor or cpu)) SAME (((interfac\$3 near6 (chip or asic or integrated)) near10 memory))))) and (@ad<19990929 or @rlad<19990929)) and (711/105.ccls. or 711/100.ccls. or 711/212.ccls.))	USPAT; US-PGPUB	2002/09/05 16:22
14	11288	((microprocessor or cpu or processor) same interfac\$4 same ((main adj1 memory) or dram or sram or sdram)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/05 16:30
15	117458	((select\$3 or multiplex\$3) near15 (port or path)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/05 16:31
16	119	((interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)) and ((microprocessor or cpu or processor) same interfac\$4 same ((main adj1 memory) or dram or sram or sdram)) and ((select\$3 or multiplex\$3) near15 (port or path))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/05 16:31
17	94	(((((interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)) and ((microprocessor or cpu or processor) same interfac\$4 same ((main adj1 memory) or dram or sram or sdram)) and ((select\$3 or multiplex\$3) near15 (port or path))) and (@ad<19990929 or @rlad<19990929))	USPAT; US-PGPUB	2002/09/05 16:44

18	6	(((((interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)) and ((microprocessor or cpu or processor) same interfac\$4 same ((main adj1 memory) or dram or sram or sdram)) and ((select\$3 or multiplex\$3) near15 (port or path))) and (@ad<19990929 or @rlad<19990929)) and ((710/305-307.ccls. or 710/309-310.ccls. or 710/313.ccls. or 710/315.ccls.) or (710/52.ccls. or 710/62-63.ccls. or 710/66.ccls. or 710/74.ccls.))	USPAT; US-PGPUB	2002/09/05 16:44
19	1	(((((interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)) and ((microprocessor or cpu or processor) same interfac\$4 same ((main adj1 memory) or dram or sram or sdram)) and ((select\$3 or multiplex\$3) near15 (port or path))) and (@ad<19990929 or @rlad<19990929)) and (711/105.ccls. or 711/100.ccls. or 711/212.ccls.))	USPAT; US-PGPUB	2002/09/05 16:45
20	87	(((((interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)) and ((microprocessor or cpu or processor) same interfac\$4 same ((main adj1 memory) or dram or sram or sdram)) and ((select\$3 or multiplex\$3) near15 (port or path))) and (@ad<19990929 or @rlad<19990929)) not ((((((interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)) and ((microprocessor or cpu or processor) same interfac\$4 same ((main adj1 memory) or dram or sram or sdram)) and ((select\$3 or multiplex\$3) near15 (port or path))) and (@ad<19990929 or @rlad<19990929)) and ((710/305-307.ccls. or 710/309-310.ccls. or 710/313.ccls. or 710/315.ccls.) or (710/52.ccls. or 710/62-63.ccls. or 710/66.ccls. or 710/74.ccls.))) not ((((((interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)) and ((microprocessor or cpu or processor) same interfac\$4 same ((main adj1 memory) or dram or sram or sdram)) and ((select\$3 or multiplex\$3) near15 (port or path))) and (@ad<19990929 or @rlad<19990929)) and (711/105.ccls. or 711/100.ccls. or 711/212.ccls.))	USPAT; US-PGPUB	2002/09/05 16:45
21	94	(((((interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)) and ((microprocessor or cpu or processor) same interfac\$4 same ((main adj1 memory) or dram or sram or sdram)) and ((select\$3 or multiplex\$3) near15 (port or path))) and (@ad<19990929 or @rlad<19990929))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/05 16:44
22	6	(((((interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)) and ((microprocessor or cpu or processor) same interfac\$4 same ((main adj1 memory) or dram or sram or sdram)) and ((select\$3 or multiplex\$3) near15 (port or path))) and (@ad<19990929 or @rlad<19990929)) and ((710/305-307.ccls. or 710/309-310.ccls. or 710/313.ccls. or 710/315.ccls.) or (710/52.ccls. or 710/62-63.ccls. or 710/66.ccls. or 710/74.ccls.))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/05 16:44

23	1	(((((interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)) and ((microprocessor or cpu or processor) same interfac\$4 same ((main adj1 memory) or dram or sram or sdram)) and ((select\$3 or multiplex\$3) near15 (port or path))) and (@ad<19990929 or @rlad<19990929)) and (711/105.ccls. or 711/100.ccls. or 711/212.ccls.))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/05 16:45
24	87	(((((interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)) and ((microprocessor or cpu or processor) same interfac\$4 same ((main adj1 memory) or dram or sram or sdram)) and ((select\$3 or multiplex\$3) near15 (port or path))) and (@ad<19990929 or @rlad<19990929)) not ((((((interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)) and ((microprocessor or cpu or processor) same interfac\$4 same ((main adj1 memory) or dram or sram or sdram)) and ((select\$3 or multiplex\$3) near15 (port or path))) and (@ad<19990929 or @rlad<19990929)) and ((710/305-307.ccls. or 710/309-310.ccls. or 710/313.ccls. or 710/315.ccls.) or (710/52.ccls. or 710/62-63.ccls. or 710/66.ccls. or 710/74.ccls.))) not ((((((interfac\$3 near6 (chip or asic or integrated)) near10 (buffer or fifo or queue)) and ((microprocessor or cpu or processor) same interfac\$4 same ((main adj1 memory) or dram or sram or sdram)) and ((select\$3 or multiplex\$3) near15 (port or path))) and (@ad<19990929 or @rlad<19990929)) and (711/105.ccls. or 711/100.ccls. or 711/212.ccls.))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2002/09/05 16:45